## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	7218	324/244,249,253,254,255,258,260.ccls. 327/110,172,176.ccls. 33/355R,356,352. ccls. 73/1.76.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/05/31 18:39
S2	785	S1 and (flux\$1gate flux adj gate (((coil winding) same (core yoke ferr\$5 ferr\$1magnet\$3 pole adj piece) and ((core yoke ferr\$5 ferr\$1magnet\$3 pole adj piece) same saturat\$5))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/06/01 16:44
S3	20	S2 and (a near2 d analog near3 digital adc micro\$1computer micro\$1controller micro\$1processor processor computer cpu) and (\$1and\$1gate \$1and adj gate \$1anding \$1and-ing \$1anded \$1and-ed) and (amplifier amp pre\$1amp\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/05/31 18:47
S4	28946	(flux\$1gate flux adj gate (((coil winding) same (core yoke ferr\$5 ferr\$1magnet\$3 pole adj piece) and ((core yoke ferr\$5 ferr\$1magnet\$3 pole adj piece) same saturat\$5))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/05/31 18:47
S5	427	S4 and (a near2 d analog near3 digital adc micro\$1computer micro\$1controller micro\$1processor processor computer cpu) and (\$1and\$1gate \$1and adj gate \$1anding \$1and-ing \$1anded \$1and-ed) and (amplifier amp pre\$1amp\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/05/31 18:48
S6	407	S5 not S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/05/31 19:31
S9	14	("4622843" "5223821" "5333110" "5376245 " "5744956" "5764052" "6282803").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/05/31 19:38
S10	450	324/253.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/06/01 15:33

## **EAST Search History**

S12	43	LEE-WOO-JONG.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/06/01 16:46
S13	91	CHOI-SANG-ON.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/06/01 17:43
S14	2	SONG-SEUNG-CHOUL.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/06/01 17:43
S15	0	324/253.ccls. and ((a near2 d analog near3 digital adc micro\$1computer micro\$1controller micro\$1processor processor computer cpu) and (\$1and\$1gate \$1and adj gate \$1anding \$1and-ing \$1anded \$1and-ed) and (amplifier amp pre\$1amp\$7)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/06/01 16:45

6/2/2006 2:36:03 PM C:\Documents and Settings\dschindler\My Documents\EAST\Workspaces\10658380AF.wsp Page 2